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Revisions			
Rev	Description	Date	Approved
X	Initial Release	10/15/2012	Jose Quiroz
X1	GPIO reassignment for SW	10/23/2012	Jose Quiroz
X2	POWER MANAGMENT RF MATCHING	10/26/2012	Jose Quiroz
A	PRODUCTION RELEASE	11/07/2012	Jose Quiroz
B	MATCHING UPDATE SYMBOL UPDATE	05/24/2013	Jose Quiroz
C	DIO0 & DIO1 EXTERNAL CONNECTIONS	07/22/2013	Jose Quiroz
D	FLOW CONTROL THROUGH J17	10/25/2013	Jose Quiroz

		<b>Wireless Connectivity Operation</b> 5501 William Cannon Drive West Austin, TX 78735-8556	
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- 54321
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1. Unless Otherwise Specified:  
All resistors are in ohms, 5%, 1/8 Watt  
All capacitors are in uF, 20%, 50V  
All voltages are DC  
All polarized capacitors are aluminum electrolytic
2. Interrupted lines coded with the same letter or letter combinations are electrically connected.
3. Device type number is for reference only. The number varies with the manufacturer.
4. Special signal usage:  
\_B Denotes - Active-Low Signal  
<> or [] Denotes - Vectored Signals
5. Interpret diagram in accordance with American National Standards Institute specifications, current revision, with the exception of logic block symbology.

			
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